

Quarantining Weakness

Compositional Reasoning Under Relaxed Memory Models

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Compositional Reasoning, Relaxed Memory

What is the interface of a concurrent object?

implicit causality via absence of interleavings ...

What changes to account for weak memory?

... explicit causality via happens-before

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Spin lock (Library)

■ Implementation

```
var v=1;  
fun rel () { v=0; }  
fun acq () { do skip until v.cas (0, 1); }
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(Initially locked)

(Strong memory)

■ Trace of release

$\langle ?\text{call rel} \rangle$ $\langle \text{wr } v \ 0 \rangle$ $\langle !\text{ret rel} \rangle$
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(Input) (Write) (Output)
(Take control) (Give control)

(Lock's viewpoint)

■ Trace of acquire

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Spin lock (Two threads)

■ Implementation

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■ Possible interleaving

(Color = thread)

$\langle ?\text{call acq} \rangle \langle \text{rd } v \ 1 \rangle \langle ?\text{call rel} \rangle \langle \text{wr } v \ 0 \rangle \langle \text{cas } v \ 0 \ 1 \rangle \langle !\text{ret acq} \rangle \langle !\text{ret rel} \rangle$

■ Impossible interleaving

$\langle ?\text{call acq} \rangle \langle \text{rd } v \ 1 \rangle \langle \text{cas } v \ 0 \ 1 \rangle \langle !\text{ret acq} \rangle \langle ?\text{call rel} \rangle \langle \text{wr } v \ 0 \rangle \langle !\text{ret rel} \rangle$

■ Looking only at I/O actions:

$\langle ?\text{call rel} \rangle$ must precede $\langle !\text{ret acq} \rangle$

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■ Abbreviate

$$\left\{ \begin{array}{l} \langle ?\text{call rel} \rangle \langle !\text{ret rel} \rangle \langle ?\text{call acq} \rangle \langle !\text{ret acq} \rangle \\ \langle ?\text{call rel} \rangle \langle ?\text{call acq} \rangle \langle !\text{ret rel} \rangle \langle !\text{ret acq} \rangle \\ \langle ?\text{call rel} \rangle \langle ?\text{call acq} \rangle \langle !\text{ret acq} \rangle \langle !\text{ret rel} \rangle \\ \langle ?\text{call acq} \rangle \langle ?\text{call rel} \rangle \langle !\text{ret rel} \rangle \langle !\text{ret acq} \rangle \\ \langle ?\text{call acq} \rangle \langle ?\text{call rel} \rangle \langle !\text{ret acq} \rangle \langle !\text{ret rel} \rangle \\ \langle ?\text{call acq} \rangle \langle !\text{ret acq} \rangle \langle ?\text{call rel} \rangle \langle !\text{ret rel} \rangle \end{array} \right\}$$

as

$\langle ?\text{call rel} \rangle \quad \langle ?\text{call acq} \rangle$
↓ ↘ ↓
 $\langle !\text{ret rel} \rangle \quad \langle !\text{ret acq} \rangle$

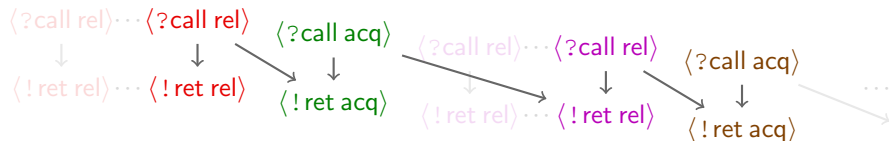
→ constrains interleavings

Spin lock (Two threads)

■ Implementation

```
var v=1;  
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```

■ Interface: set of traces obeying



- \longrightarrow constrains interleavings
- \longrightarrow imposed by Lock

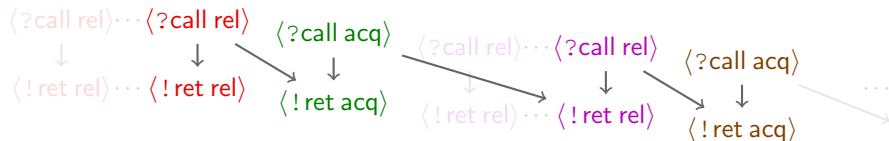
■ What about client?

Spin lock (Two threads)

■ Implementation

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■ What about client?

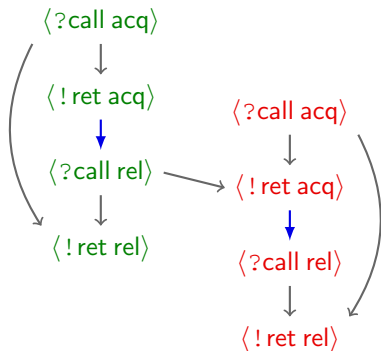
Spin lock (Client constraints)

■ Implementation

```
var v=0;  
fun rel () { v=0; }  
fun acq () { do skip until v.cas (0, 1); }
```

(Initially unlocked)

■ Example of client order Multiple calls from single thread



Two kinds of constraints

- ? → ! Imposed by library (In → out)
- ! → ? Imposed by client (Out → in)

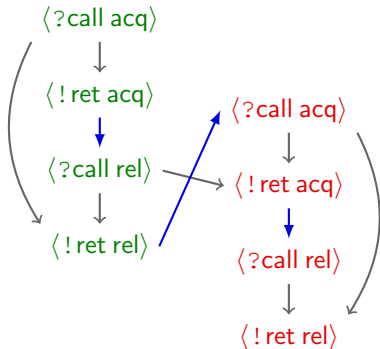
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Arrows constrain interleavings

More arrows = smaller set

Fully constrained = singleton,
sequential

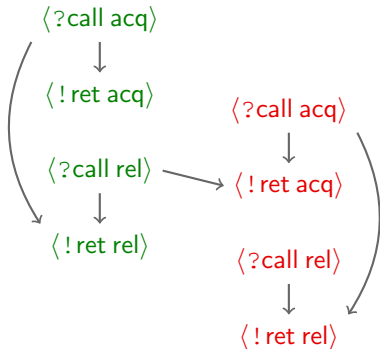
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Arrows constrain interleavings

More arrows = smaller set

No constraints = all allowed interleavings

Two locks (for one place buffer)

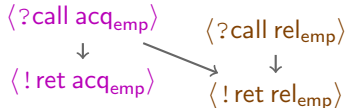
(Initially locked)

```
var vfull=1;  
fun relfull () { vfull=0; }  
fun acqfull () { ... vfull.cas(0, 1); }
```

(Initially unlocked)

```
var vemp=0;  
fun relemp () { vemp=0; }  
fun acqemp () { ... vemp.cas(0, 1); }
```

■ Locks are independent



■ Client may create dependency

Two locks (for one place buffer)

(Initially locked)

```
var vfull=1;
```

```
fun relfull () { vfull=0; }
```

```
fun acqfull () { ... vfull.cas(0, 1); }
```

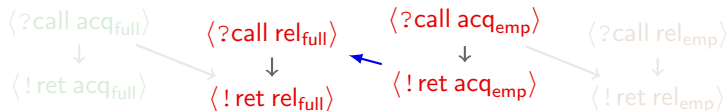
(Initially unlocked)

```
var vemp=0;
```

```
fun relemp () { vemp=0; }
```

```
fun acqemp () { ... vemp.cas(0, 1); }
```

■ Locks are independent



■ Client may create dependency

One place buffer (Initially empty)

```
var x=0;
```

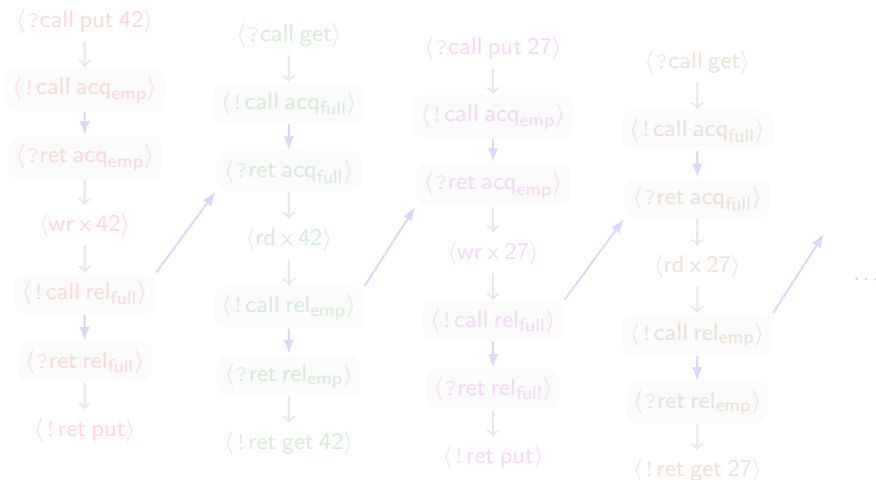
```
fun put (r) { acqemp (); x=r; relfull (); }
```

```
fun get () { acqfull (); let r=x; relemp (); return r; }
```

(r = register)

(emp unlocked)

(full locked)



One place buffer (Initially empty)

```
var x=0;
```

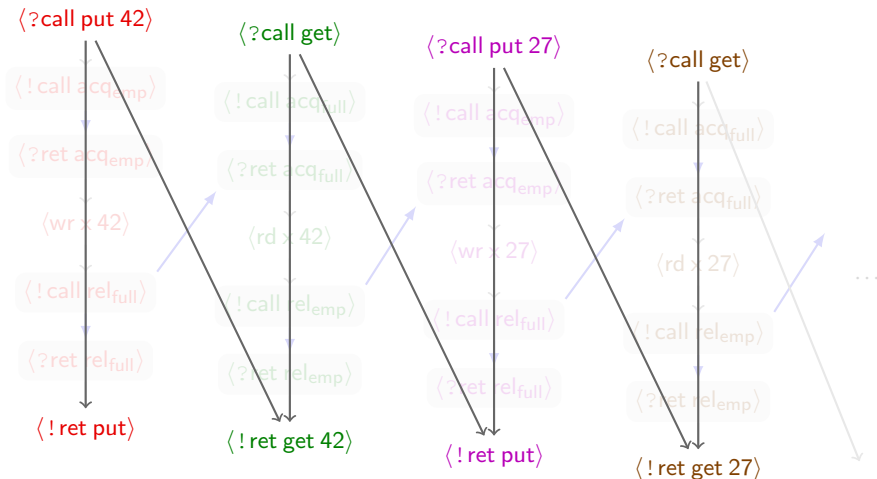
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One place buffer

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```

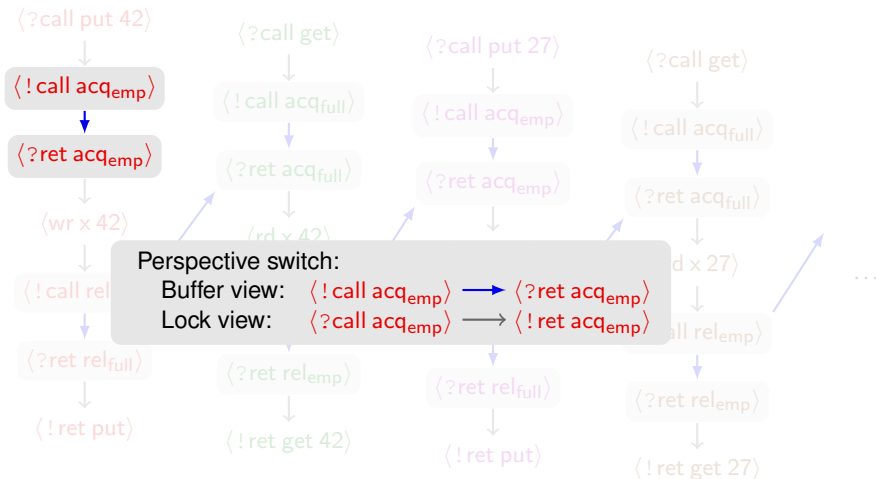
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fun put (r) { acq_emp (); x=r; rel_full (); }
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One place buffer (Client interface)

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```

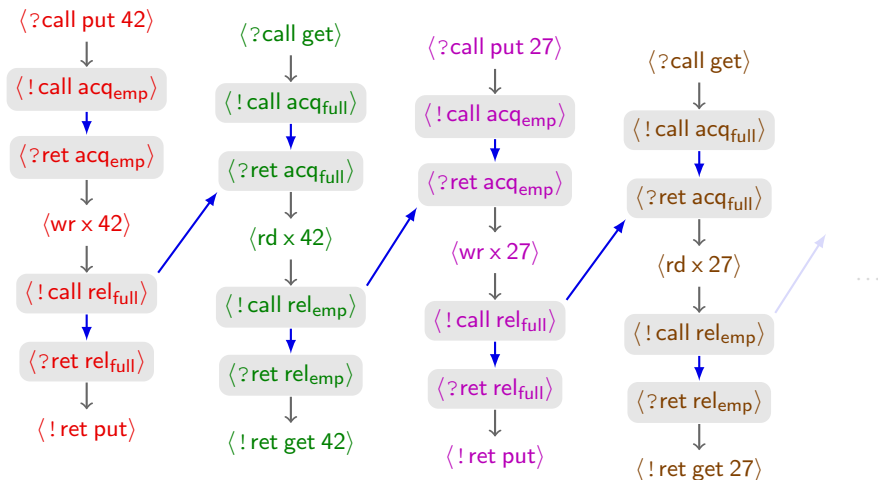
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One place buffer (Memory actions)

```
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```

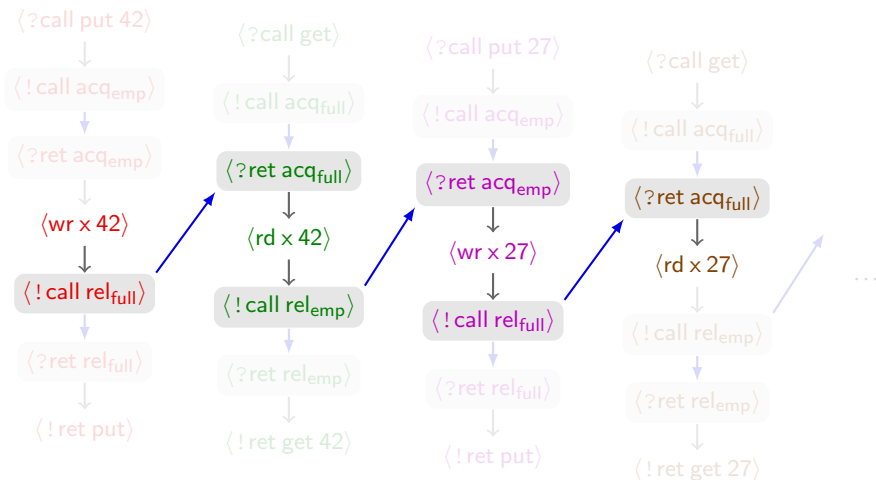
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One place buffer (Relaxed memory)

```
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```

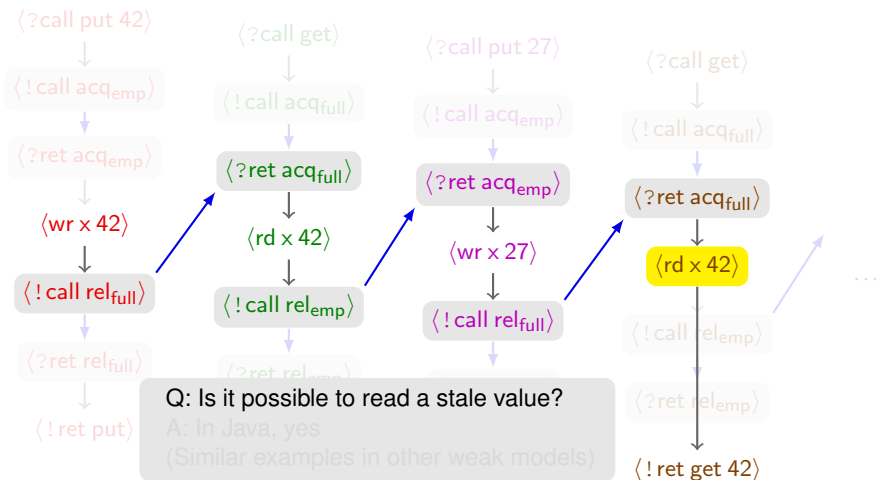
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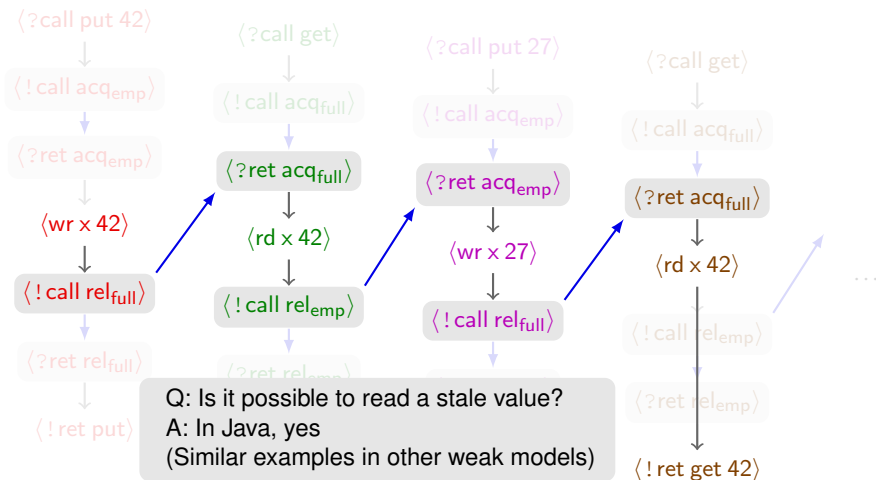
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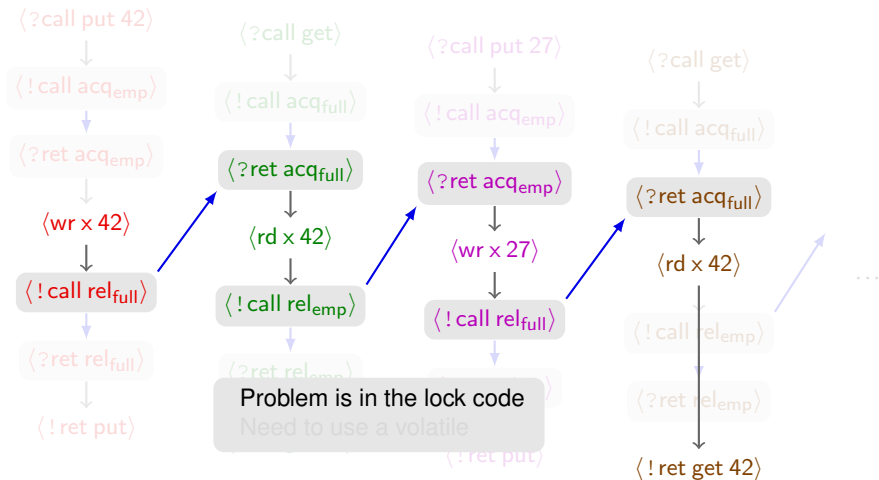
One place buffer (Relaxed memory)

```
var v=1;
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```
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```

```
fun acq () { do skip until v.cas(0, 1); }
```

(Lock code)



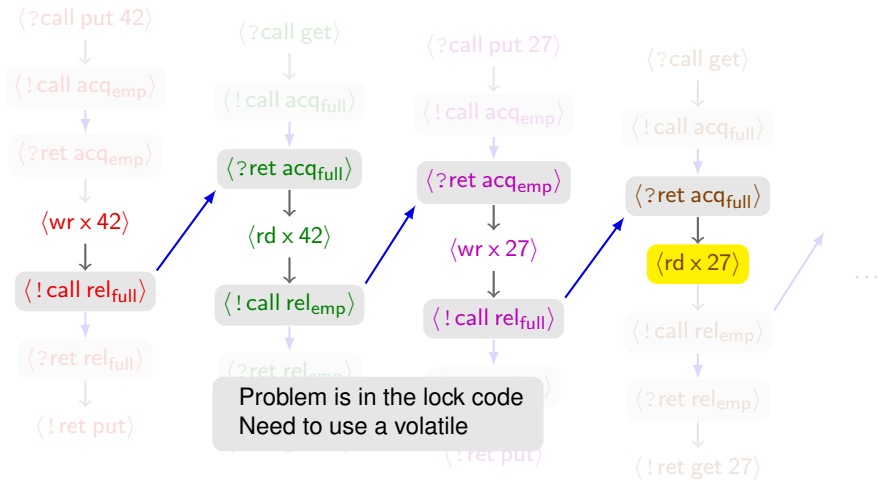
One place buffer (Relaxed memory)

```
volatile v=1;
```

```
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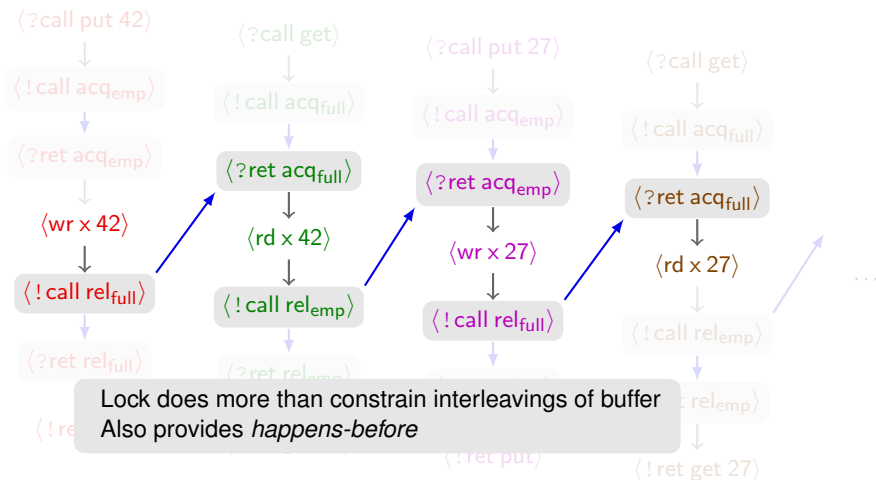
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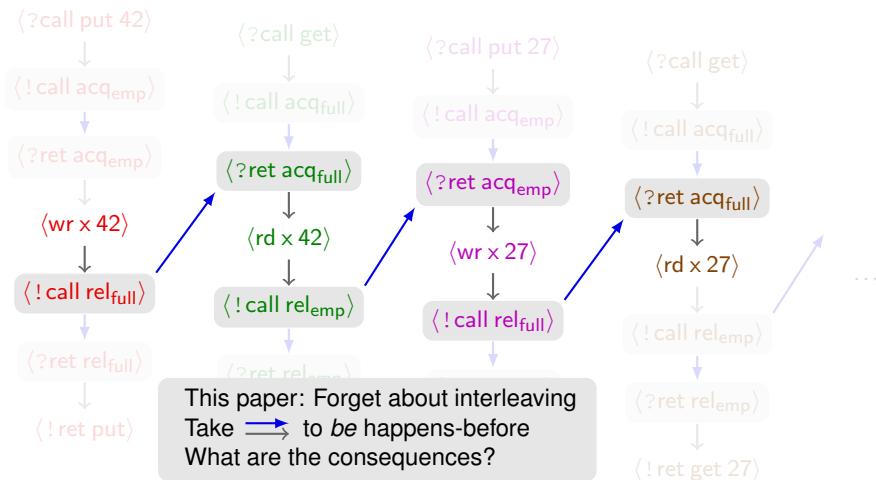
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Traditional notions of correctness

Happens-before

Results

Compositionality

Notions of correctness

- Sequential consistency (SC) = methods appear atomic (Lamport, IEEE Trans. Comput. 1979)

$$(\forall \sigma \in \text{Impl}) (\exists \phi \in \text{SequentialSpec}) (\forall s \in \text{Thread}) \sigma|_s = \phi|_s$$

- Linearizability = Serializability + compositionality (Herlihy/Wing, POPL 1987, TOPLAS 1990)

... and ϕ must respect order of nonoverlapping calls in σ

- Example ($\text{Impl} \sqsubseteq \text{Spec}$)



That is,

$$\{\langle ?call f \rangle \langle !ret f \rangle \langle ?call g \rangle \langle !ret g \rangle\} \sqsubseteq \{\langle ?call g \rangle \langle !ret g \rangle \langle ?call f \rangle \langle !ret f \rangle\}$$

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Philosophy

- **Serializability and Linearizability**

(Filipović/O’Hearn/Rinetzky/Yang, ESOP 2009, TCS 2010)

- **Standard view**

“the illusion of atomicity”

- **Alternate view**

“conservative over-approximations of dependencies . . .
that may arise in some client programs”

- **Strong memory: views are interderivable**

global store = global clock

- **Weak memory: views are distinct**

no global store = no global clock

temporal coincidence \neq causality

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Happens-before

- Semantics as sets Σ, Φ of traces σ, ϕ with named actions
 - Four memory models: $\mathcal{M} \in \{\text{strong, tso, pso, jmm}\}$
 - Order recovered by relation $i <_{\mathcal{M}}^{\sigma} j$
 - Informally $(<_{\mathcal{M}}^{\sigma}) = (\implies)$ (only one relation, color distinguishes polarity)

- In a specification:

$$\begin{aligned}\langle ?\text{call } f \rangle &\longrightarrow \langle !\text{ret } g \rangle \text{ if } \langle ?\text{call } f a \rangle \cdots \langle !\text{ret } g \{a\} \rangle \\ \langle !\text{ret } f \rangle &\longrightarrow \langle ?\text{call } g \rangle \text{ if } \langle !\text{ret } f b \rangle \cdots \langle ?\text{call } g \{b\} \rangle\end{aligned}$$

- In opsem (thread s , actions a, b , volatile v):

$$\begin{aligned}\langle s a \rangle &\longrightarrow \langle s b \rangle \text{ if } \langle s a \rangle \cdots \langle s b \rangle \quad (\text{thread order}) \\ \langle \text{wr } v \rangle &\longrightarrow \langle \text{rd } v \rangle \text{ if } \langle \text{wr } v \rangle \cdots \langle \text{rd } v \rangle \quad (\text{synchronization}) \\ \langle \text{wr } v \rangle &\longrightarrow \langle \text{cas } v \rangle \text{ if } \langle \text{wr } v \rangle \cdots \langle \text{cas } v \rangle \quad (\text{synchronization}) \\ \langle \text{cas } v \rangle &\longrightarrow \langle \text{rd } v \rangle \text{ if } \langle \text{cas } v \rangle \cdots \langle \text{rd } v \rangle \quad (\text{synchronization}) \\ \langle \text{cas } v \rangle &\longrightarrow \langle \text{cas } v \rangle \text{ if } \langle \text{cas } v \rangle \cdots \langle \text{cas } v \rangle \quad (\text{synchronization})\end{aligned}$$

- This defines $<_{\text{jmm}}^{\sigma}$
 - $<_{\text{strong}}^{\sigma}$ includes conflicts on all variables
 - $<_{\text{tso}}^{\sigma}$ and $<_{\text{pso}}^{\sigma}$ in between

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$$\begin{aligned}\langle ?\text{call } f \rangle &\longrightarrow \langle !\text{ret } g \rangle \text{ if } \langle ?\text{call } f a \rangle \dots \langle !\text{ret } g \{a\} \rangle \\ \langle !\text{ret } f \rangle &\longrightarrow \langle ?\text{call } g \rangle \text{ if } \langle !\text{ret } f b \rangle \dots \langle ?\text{call } g \{b\} \rangle\end{aligned}$$

- In opsem (thread s , actions a, b , volatile v):

$$\begin{aligned}\langle s a \rangle &\longrightarrow \langle s b \rangle \text{ if } \langle s a \rangle \dots \langle s b \rangle && \text{(thread order)} \\ \langle \text{wr } v \rangle &\longrightarrow \langle \text{rd } v \rangle \text{ if } \langle \text{wr } v \rangle \dots \langle \text{rd } v \rangle && \text{(synchronization)} \\ \langle \text{wr } v \rangle &\longrightarrow \langle \text{cas } v \rangle \text{ if } \langle \text{wr } v \rangle \dots \langle \text{cas } v \rangle && \text{(synchronization)} \\ \langle \text{cas } v \rangle &\longrightarrow \langle \text{rd } v \rangle \text{ if } \langle \text{cas } v \rangle \dots \langle \text{rd } v \rangle && \text{(synchronization)} \\ \langle \text{cas } v \rangle &\longrightarrow \langle \text{cas } v \rangle \text{ if } \langle \text{cas } v \rangle \dots \langle \text{cas } v \rangle && \text{(synchronization)}\end{aligned}$$

- This defines $<_{\text{jmm}}^{\sigma}$
 - $<_{\text{strong}}^{\sigma}$ includes conflicts on all variables
 - $<_{\text{tso}}^{\sigma}$ and $<_{\text{pso}}^{\sigma}$ in between

Happens-before

- Semantics as sets Σ, Φ of traces σ, ϕ with named actions
 - Four memory models: $\mathcal{W} \in \{\text{strong, tso, pso, jmm}\}$
 - Order recovered by relation $i <_{\mathcal{W}}^{\sigma} j$
 - Informally $(<_{\mathcal{W}}^{\sigma}) = (\implies)$ (only one relation, color distinguishes polarity)

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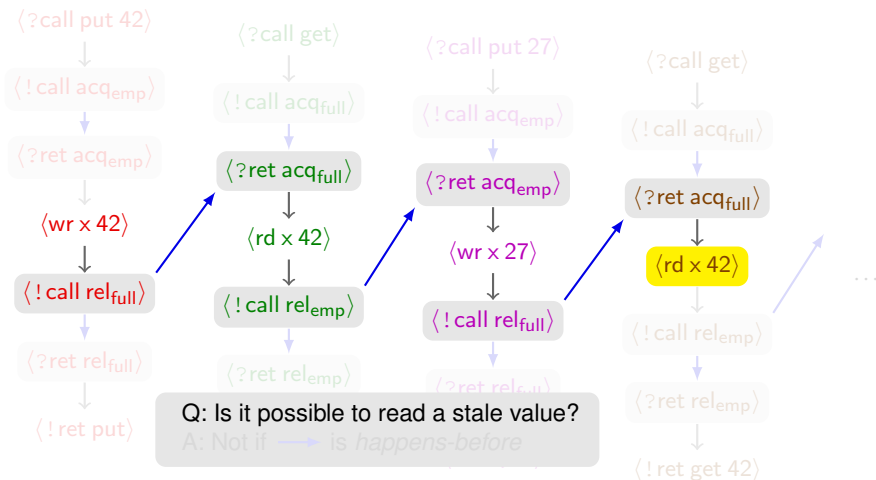
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One place buffer (Revisited)

```
var x=0;
```

```
fun put (r) { acqemp (); x=r; relfull (); }
```

```
fun get () { acqfull (); let r=x; relemp (); return r; }
```

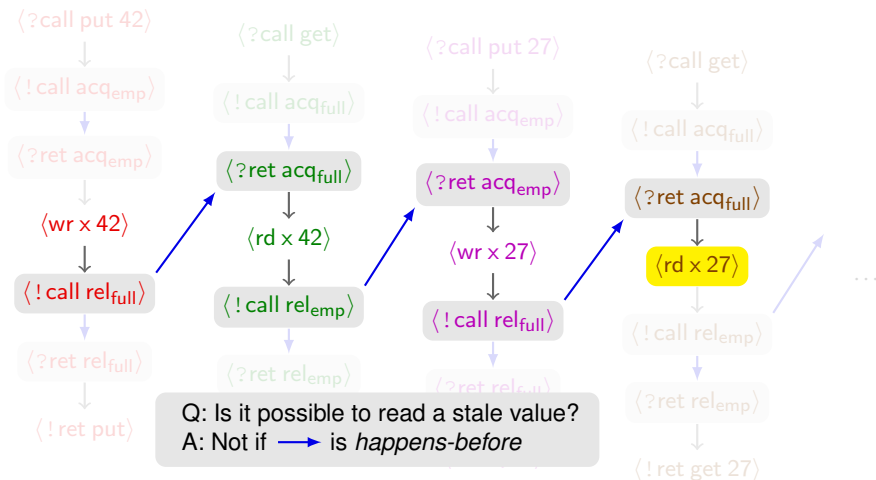


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Details

Define $\Sigma \sqsubseteq_{\text{Lin}} \Phi$ as

$\forall \sigma \in \Sigma.$

$\exists \phi \in \Phi.$

$\exists \pi : [1 \dots |\sigma|] \rightarrow [1 \dots |\phi|].$

if either $\sigma_i, \sigma_{\pi(i)}$ is $\langle ? \rangle, \langle ! \rangle$ then $\sigma_i = \phi_{\pi(i)}$

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σ has same I/O actions as ϕ

σ has same thread order as ϕ

nonoverlapping order of σ respected by ϕ

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Refinement theorems (client and library have disjoint variables)

■ Theorem (Filipović/O'Hearn/Rinetzky/Yang, ESOP 2009)

if $\Sigma \sqsubseteq_{\text{strong}} \Phi$ $\sqsubseteq =$ linearizability
then $\llbracket P \rrbracket(\Sigma) \sqsubseteq \llbracket P \rrbracket(\Phi)$ $\sqsubseteq =$ observational refinement

■ Theorem (Burckhardt/Gotsman/Musuvathi/Yang, ESOP 2012)

if $\llbracket Q_{\text{impl}} \rrbracket \sqsubseteq_{\text{tso}} \llbracket Q_{\text{spec}} \rrbracket$
then $\llbracket P \parallel Q_{\text{impl}} \rrbracket \sqsubseteq \llbracket P \parallel Q_{\text{spec}} \rrbracket$

■ Theorem (This paper)

if $\llbracket Q \rrbracket \sqsubseteq_{\not\sim} \Phi_Q$
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Operational composition
Operational spec

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Arbitrary spec for library

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Arbitrary spec for library
Explicit tensor

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if library satisfies spec
and client correct using spec
then composed system correct
 $\mathscr{W} \in \{\text{strong, tso, pso, jmm}\}$

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- Corollary (This paper)

if $\llbracket Q \rrbracket \sqsubseteq_{\not\sim} \Phi_Q$
and $\llbracket P \rrbracket \otimes \Phi_Q \sqsubseteq_{\text{strong}} \sqsubseteq_{\not\sim} \Phi_P$
and $\llbracket P \rrbracket$ is locally SC, ...
then $\llbracket P \parallel Q \rrbracket \sqsubseteq_{\not\sim} \Phi_P$

Well synchronized clients are not affected by races in library
(Details in proceedings)

Compositionality

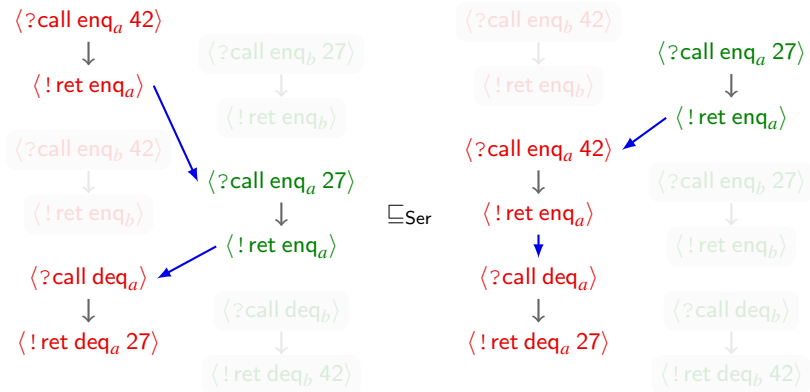
if $\llbracket P_i \rrbracket \sqsubseteq_{\mathcal{W}} \Sigma_i$ then $\llbracket P_1 \parallel P_2 \rrbracket \sqsubseteq_{\mathcal{W}} \Sigma_1 \otimes \Sigma_2$

When does it hold?

(P_i have disjoint variables)
(Not a corollary of refinement)

Compositionality counterexample (Herlihy/Wing)

Serializable trace:



\rightarrow = "non-overlapping": return before call.

Compositionality counterexample (Herlihy/Wing)

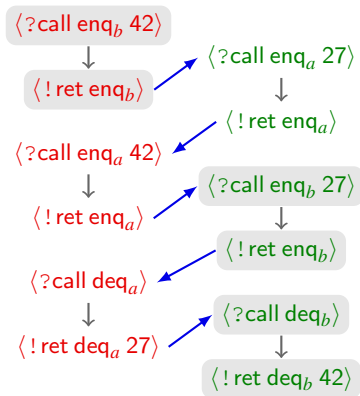
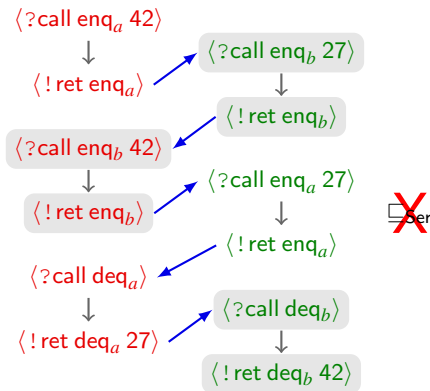
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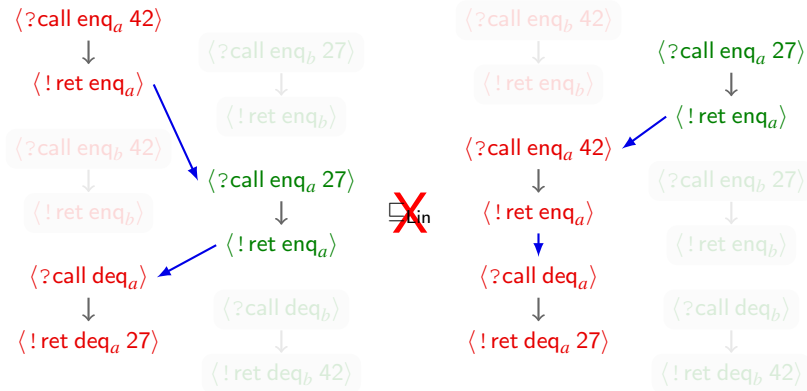
Non-Serializable trace:



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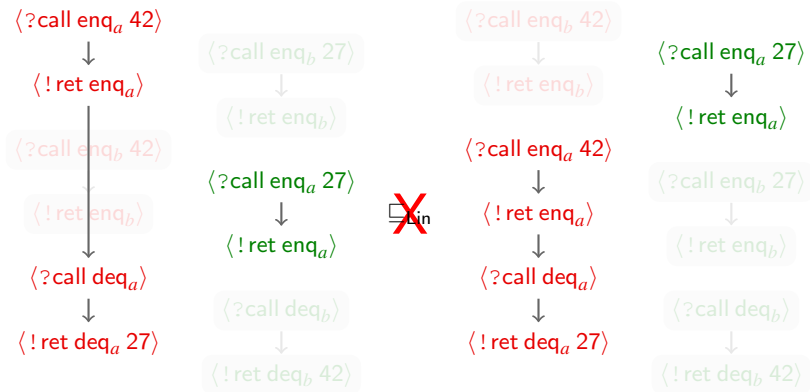
Non-Linearizable trace:



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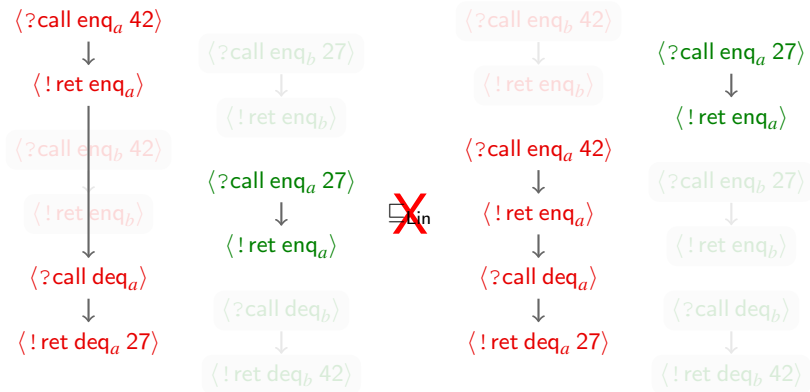


Crucial point:

$\{\langle ? f \rangle \langle ! f \rangle \langle ? g \rangle \langle ! g \rangle \langle ? h \rangle \langle ! h \rangle\} \not\subseteq_{\text{Lin}} \{\langle ? g \rangle \langle ! g \rangle \langle ? f \rangle \langle ! f \rangle \langle ? h \rangle \langle ! h \rangle\}$

Compositionality counterexample (Herlihy/Wing)

Non-Linearizable trace:

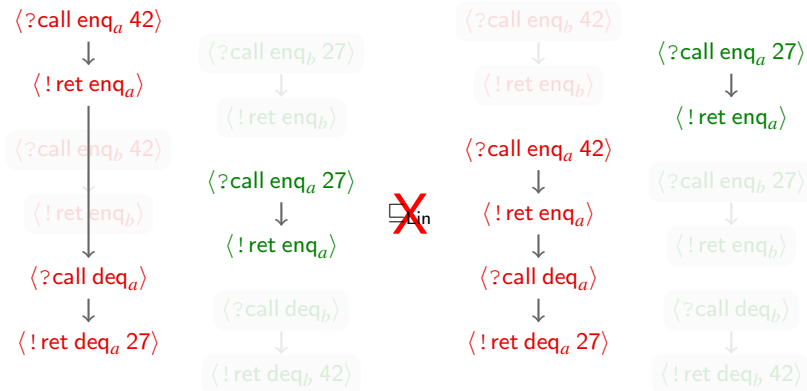


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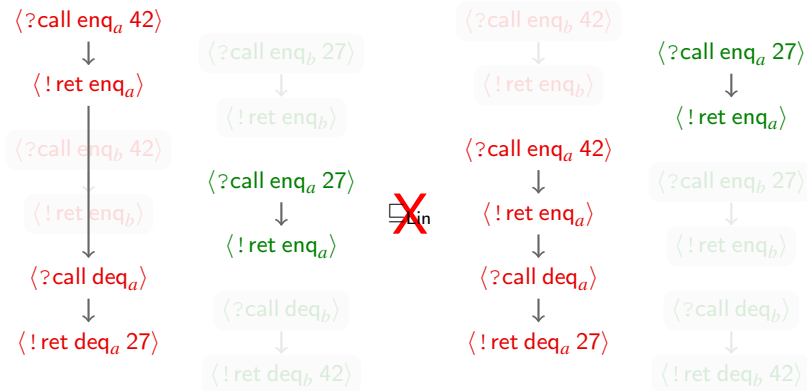


By our definition:

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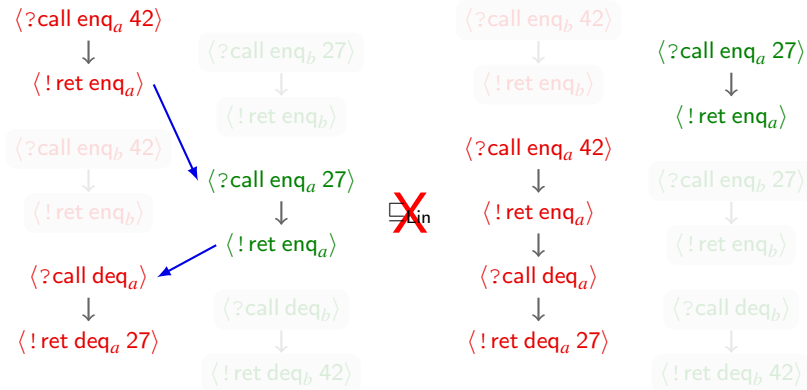


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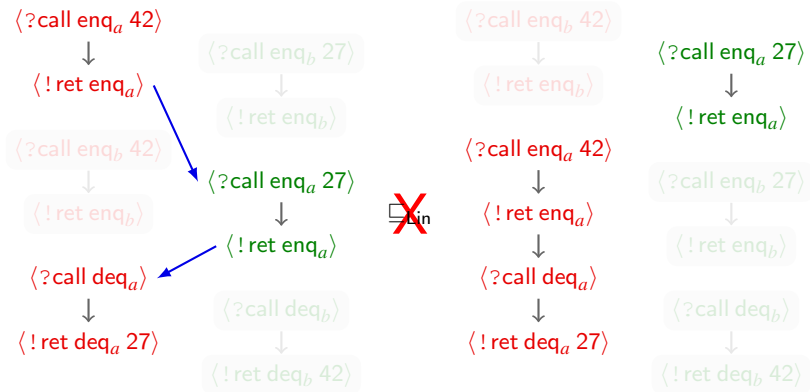


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Compositionality counterexample (Herlihy/Wing)

Non-Linearizable trace:



In the absence of the *happens-before* edge $\langle !g \rangle \rightarrow \langle ?f \rangle$, is this:

$$\{ \langle ?g \rangle \langle !g \rangle \langle ?f \rangle \langle !f \rangle \langle ?h \rangle \langle !h \rangle \}$$

a reasonable spec?

“Accidental” versus “essential” order

- Same under strong memory, not weak
- Two paths to compositionality
 - Ban accidental order in specs (closure property)
 - Reintroduce global clock
- Second path may be the only path in language with “weak” cas
(weak cas = synchronization w/o happens-before)

$$\{ \langle ? g \rangle \langle ! g \rangle \langle ? f \rangle \langle ! f \rangle \langle ? h \rangle \langle ! h \rangle \} \text{ versus } \left\{ \begin{array}{l} \langle ? g \rangle \langle ! g \rangle \langle ? f \rangle \langle ! f \rangle \langle ? h \rangle \langle ! h \rangle \\ \langle ? f \rangle \langle ! f \rangle \langle ? g \rangle \langle ! g \rangle \langle ? h \rangle \langle ! h \rangle \\ \langle ? f \rangle \langle ! f \rangle \langle ? h \rangle \langle ! h \rangle \langle ? g \rangle \langle ! g \rangle \end{array} \right\}$$

Summary

- Compositional reasoning, relaxed memory
- Linearizability using trace sets and happens-before
 - Refinement theorem
 - For strong, tso, pso and jmm
 - Symmetry between “client” and “library”
- Definition of “local” sequential consistency (and local DRF)
 - Refinement theorem for special cases using $\sqsubseteq_{\text{strong}}$ to verify client

(For online version, google “James Riely”)